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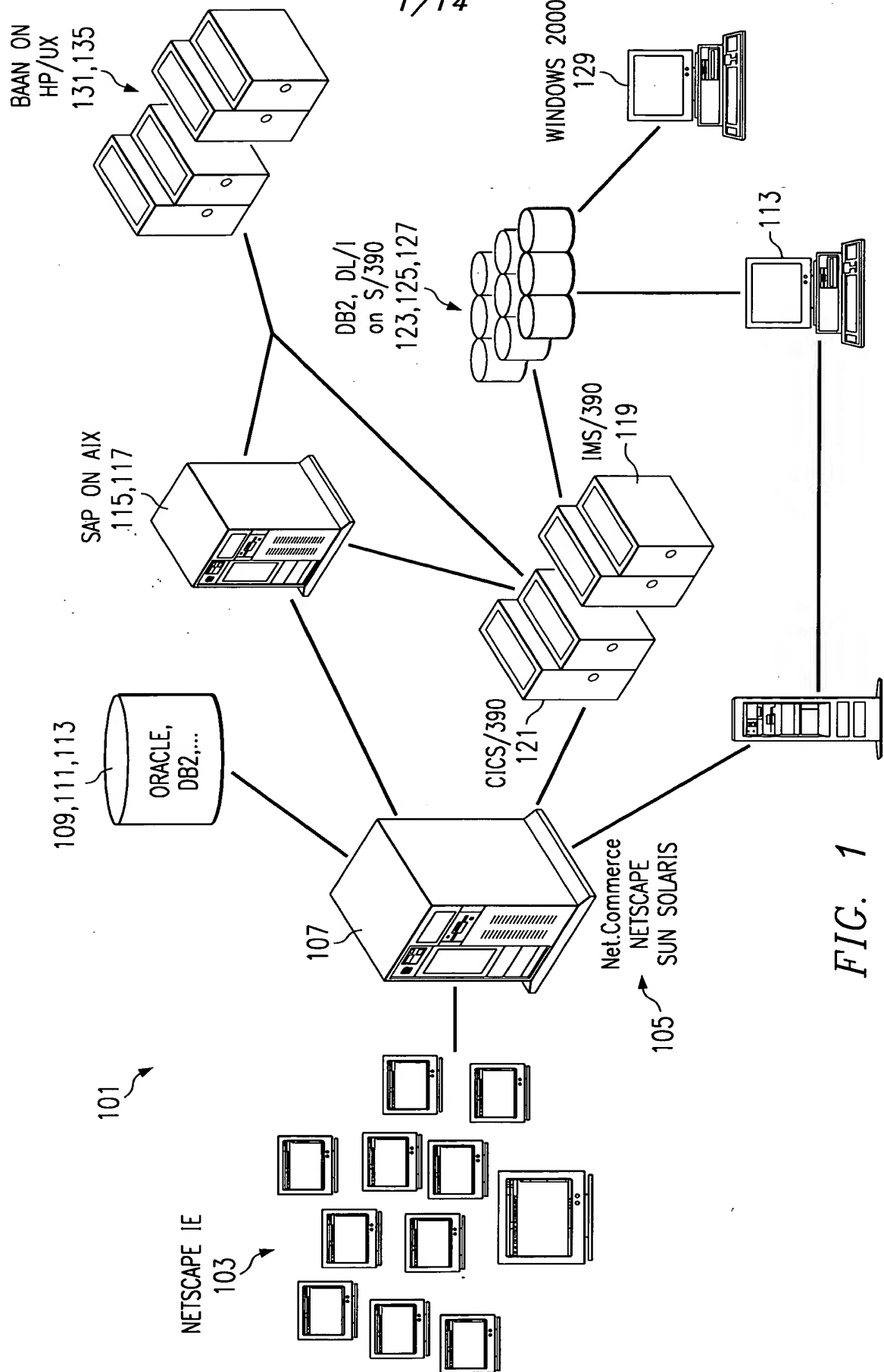


FIG. 1

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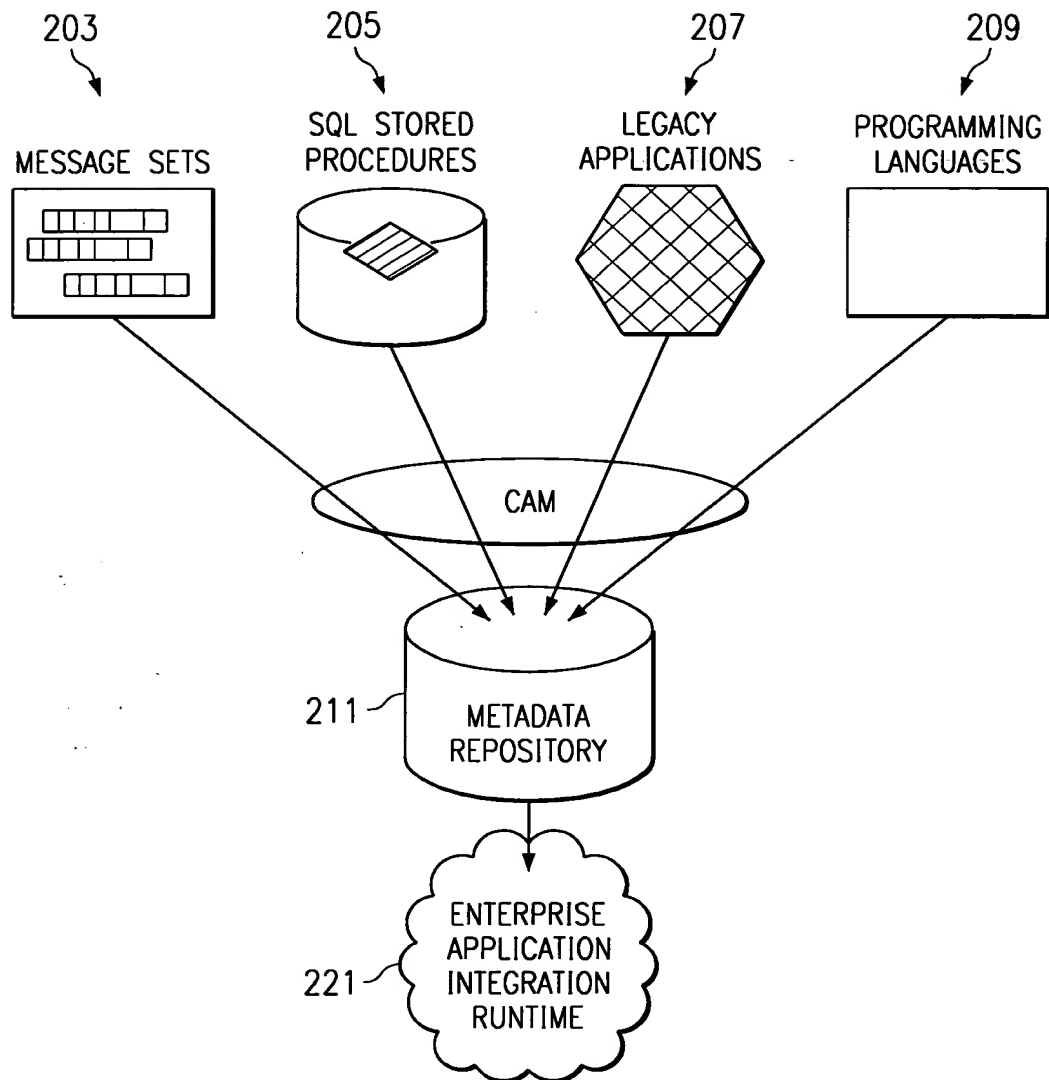


FIG. 2

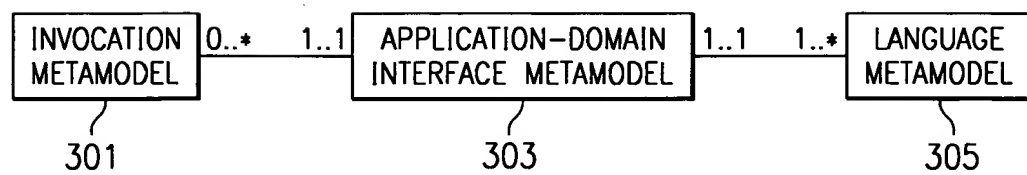
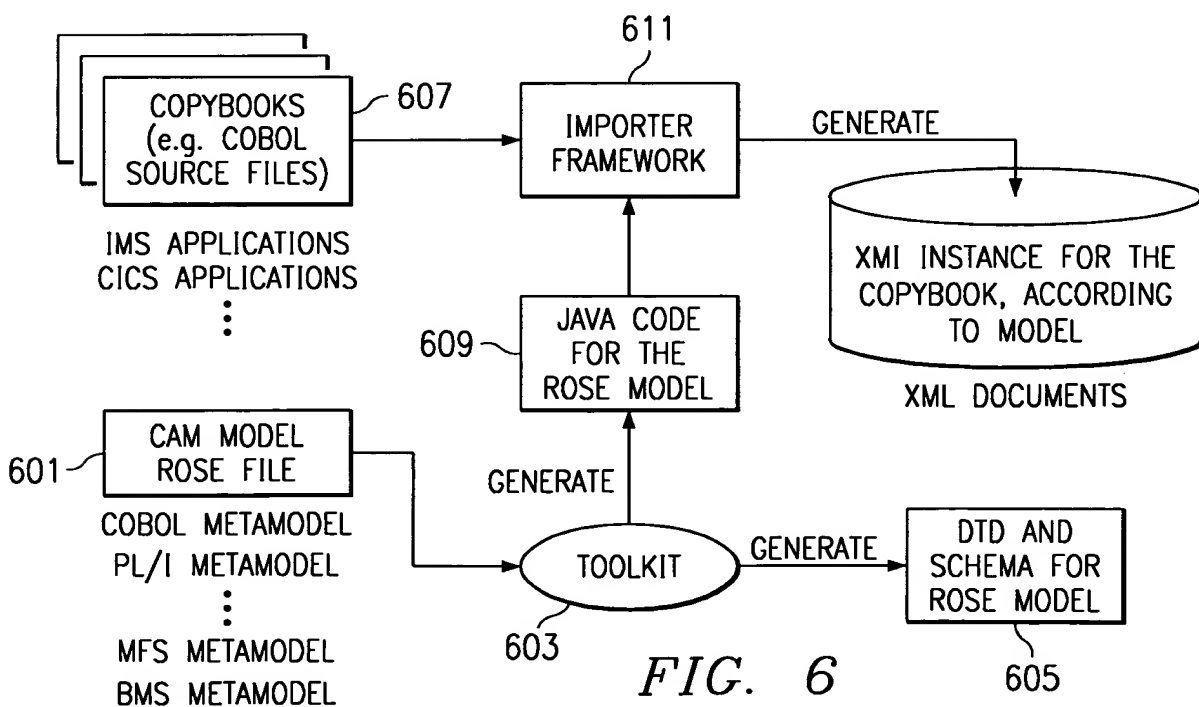
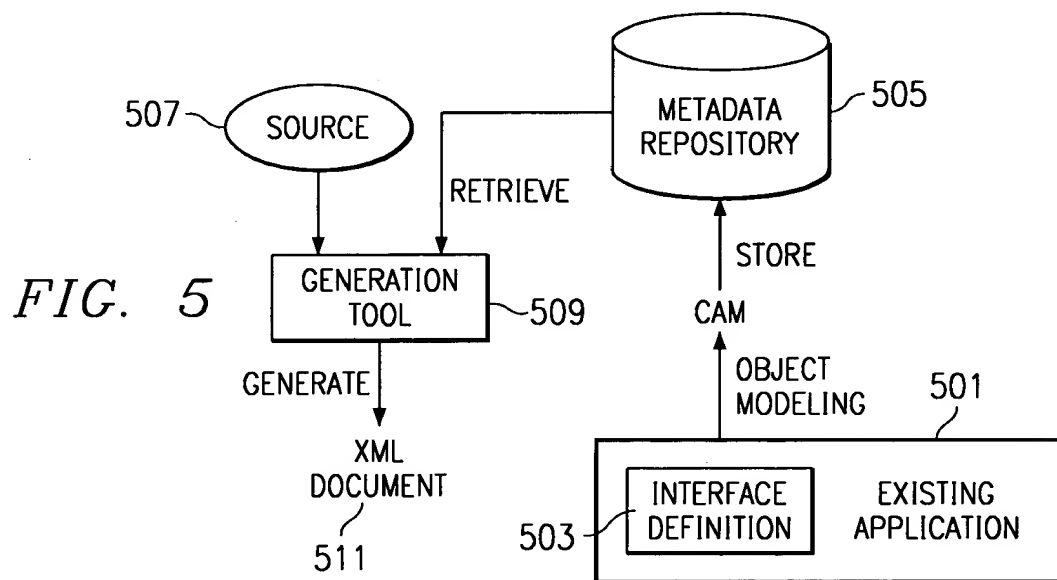
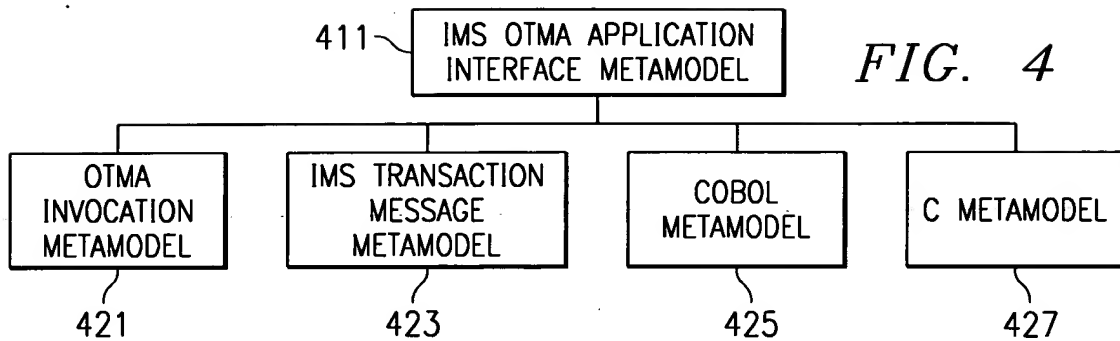


FIG. 3

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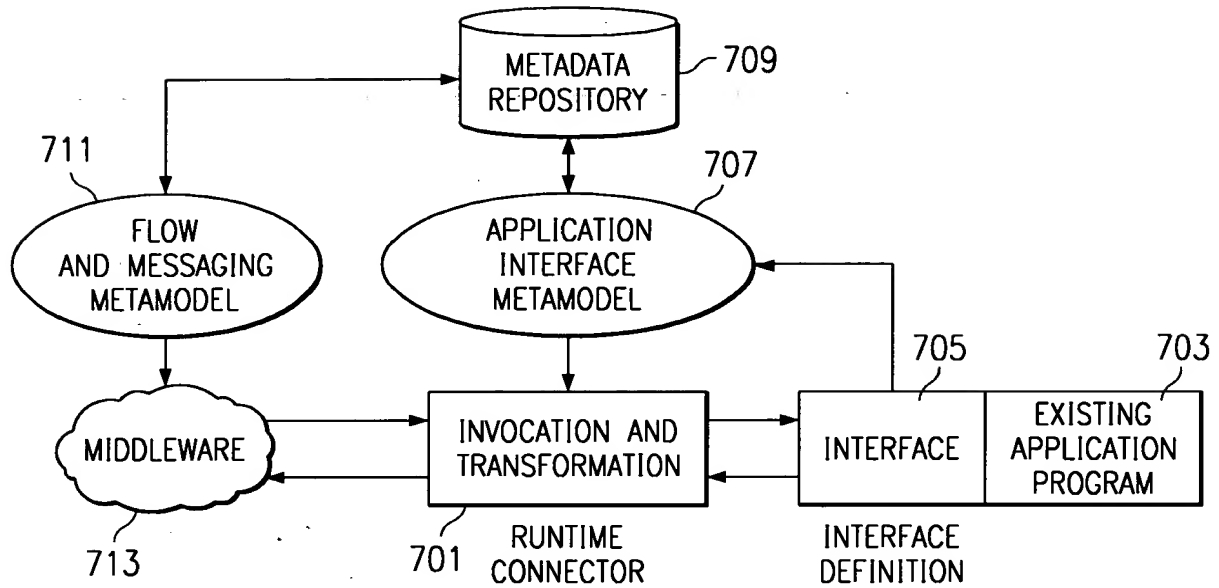


FIG. 7

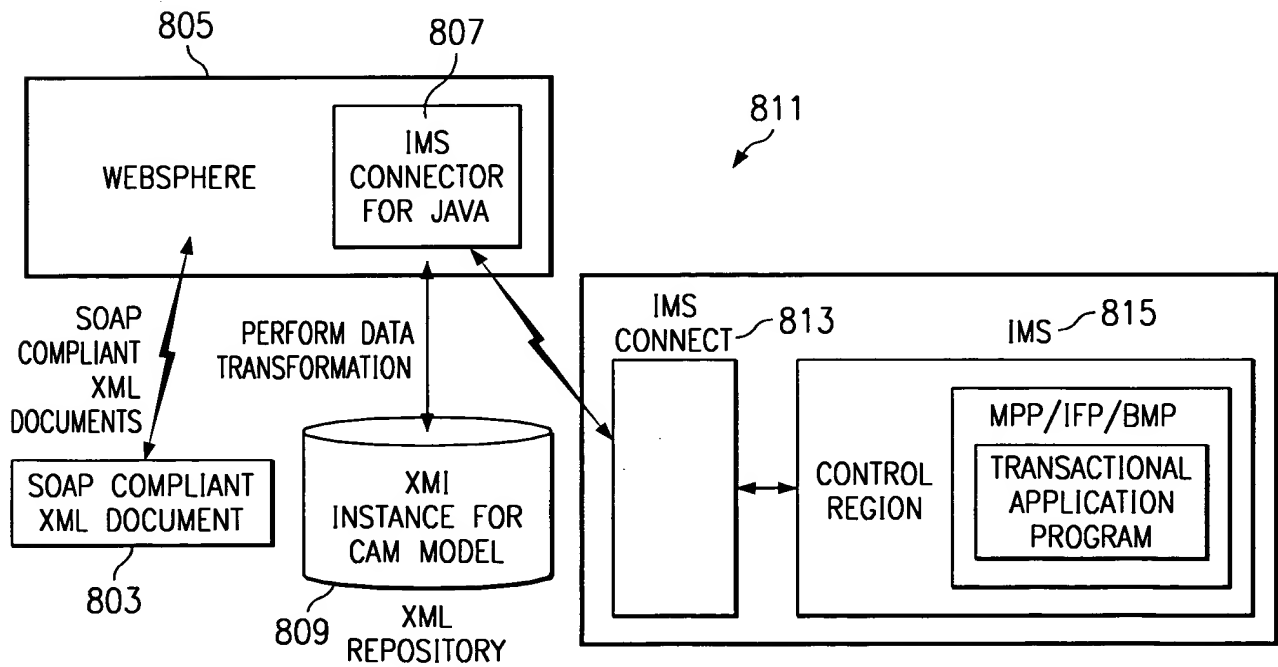
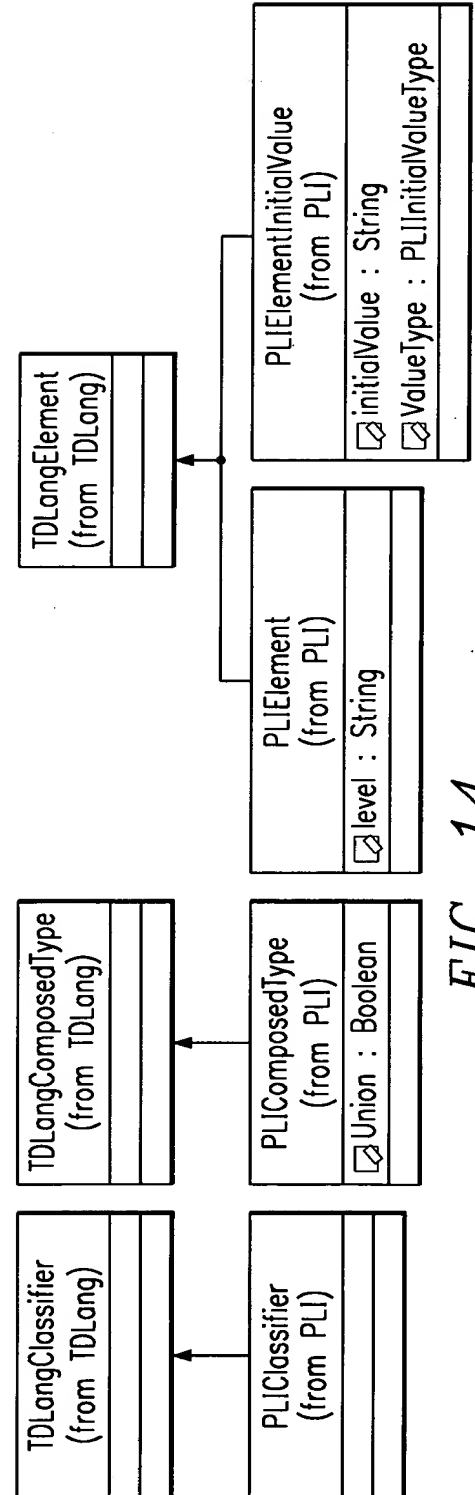
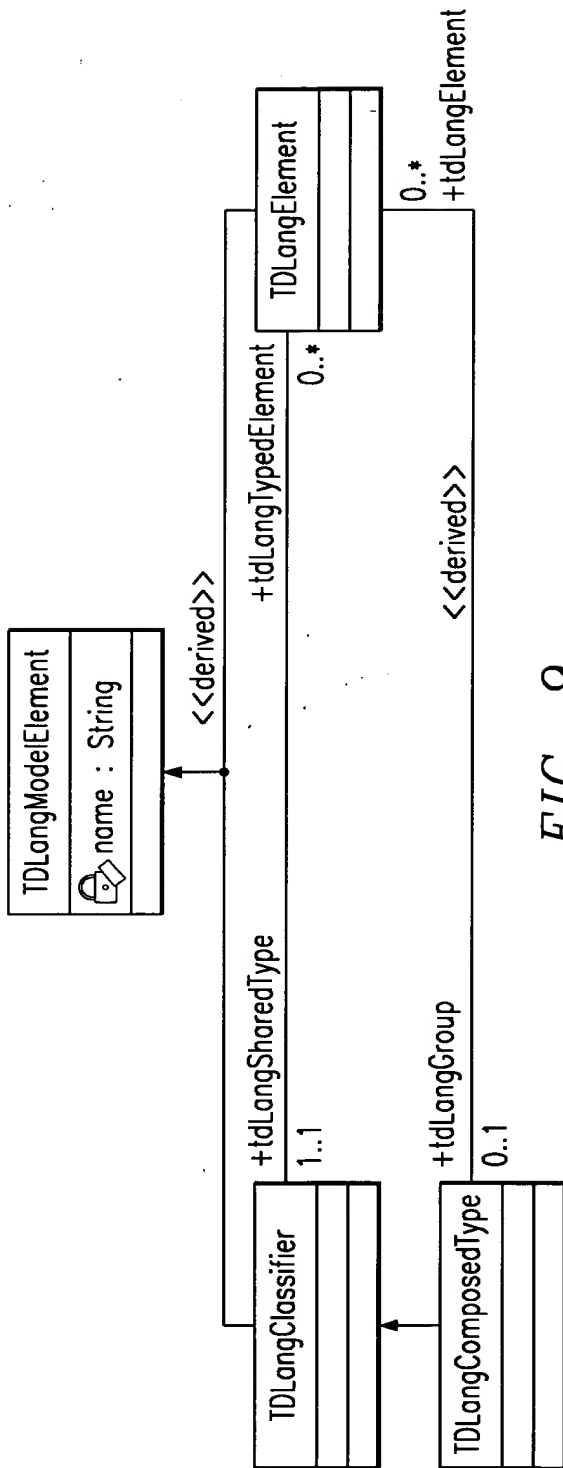


FIG. 8

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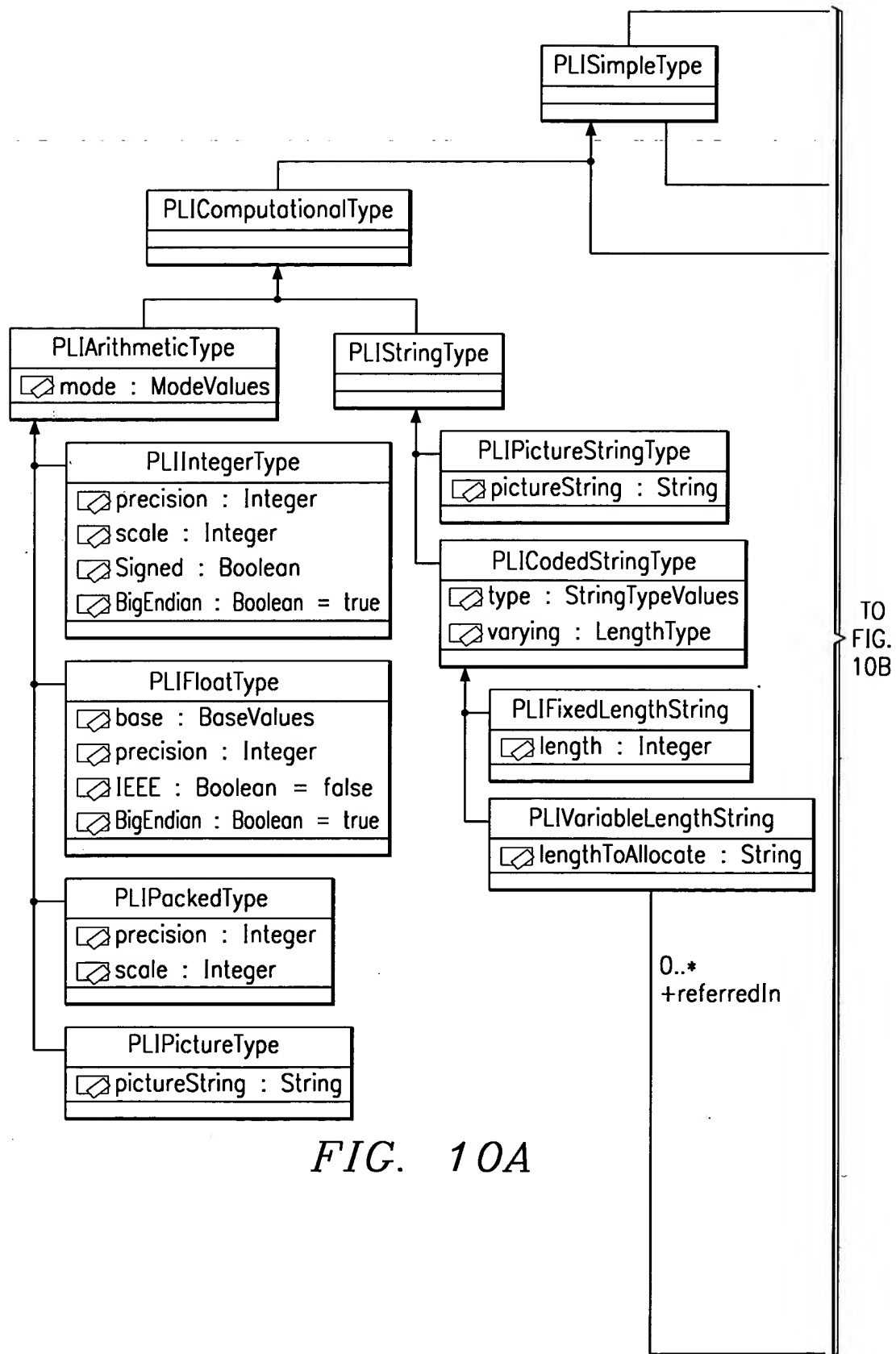


FIG. 10A

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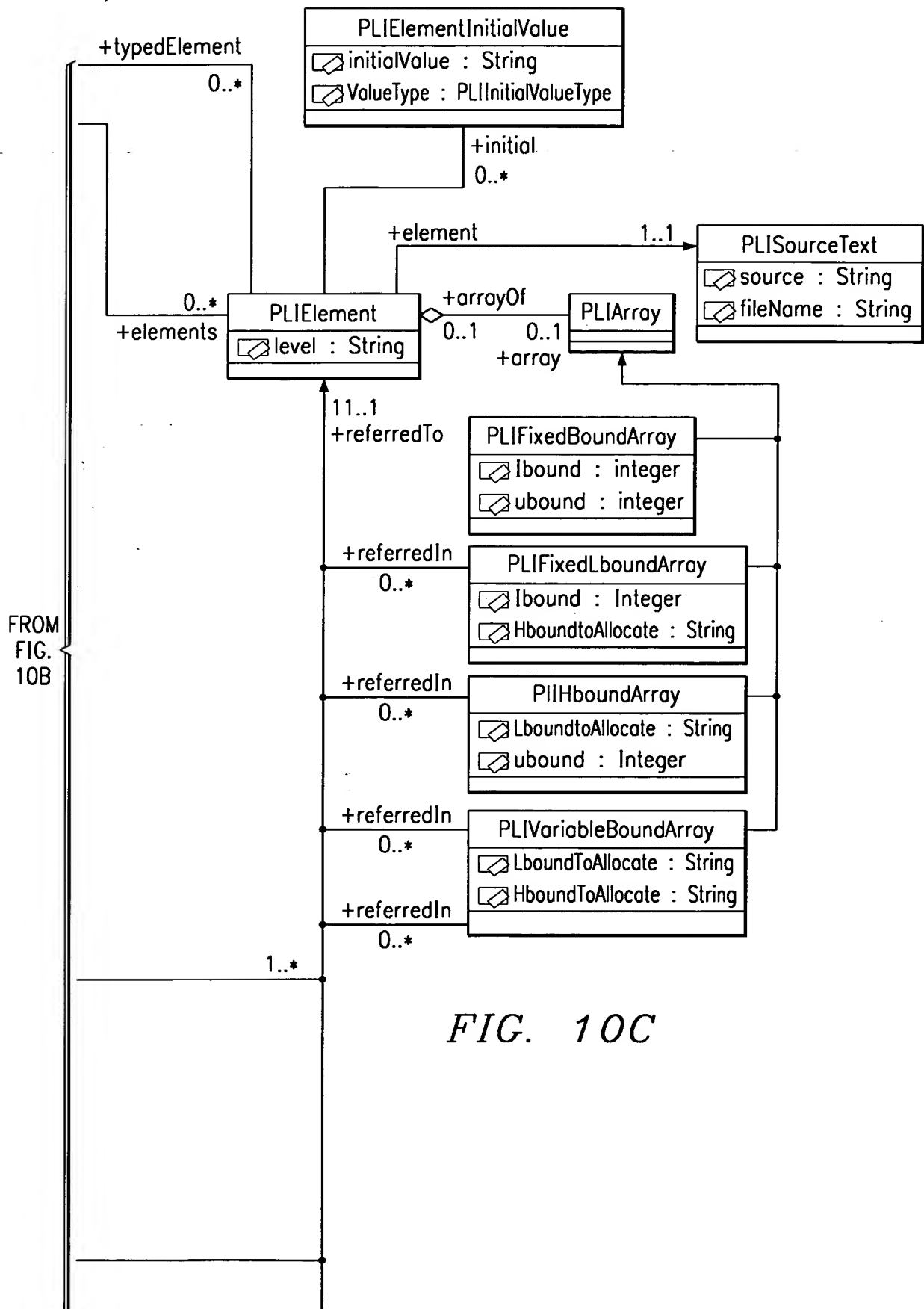


FIG. 10C

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FIG. 11

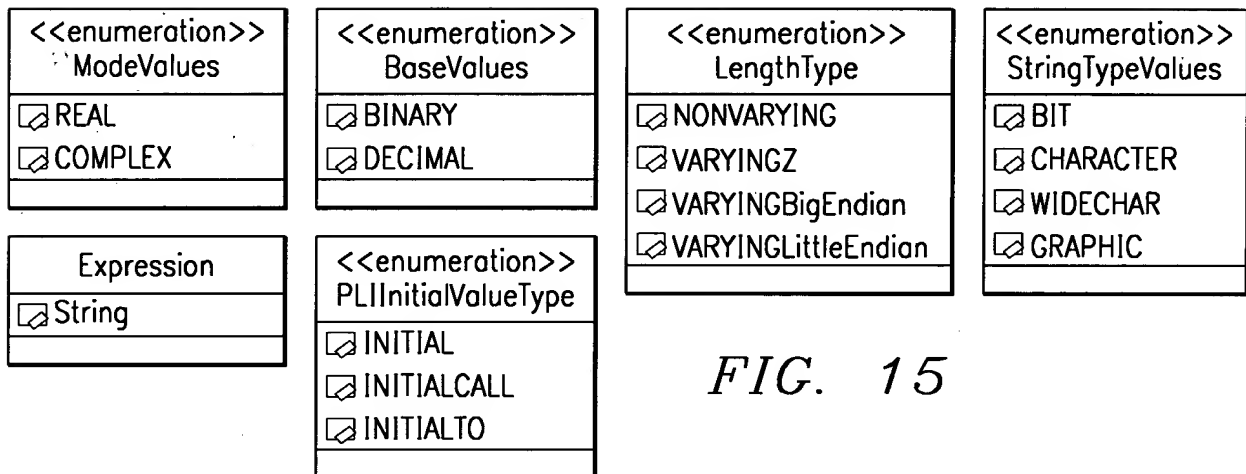
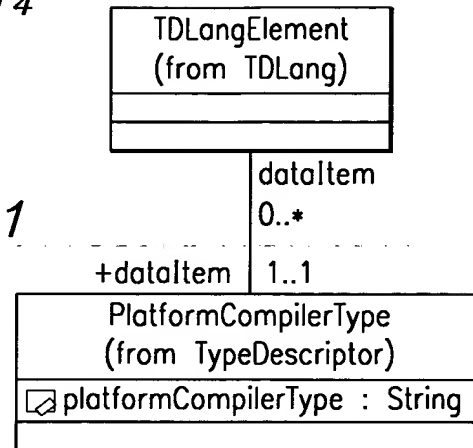


FIG. 15

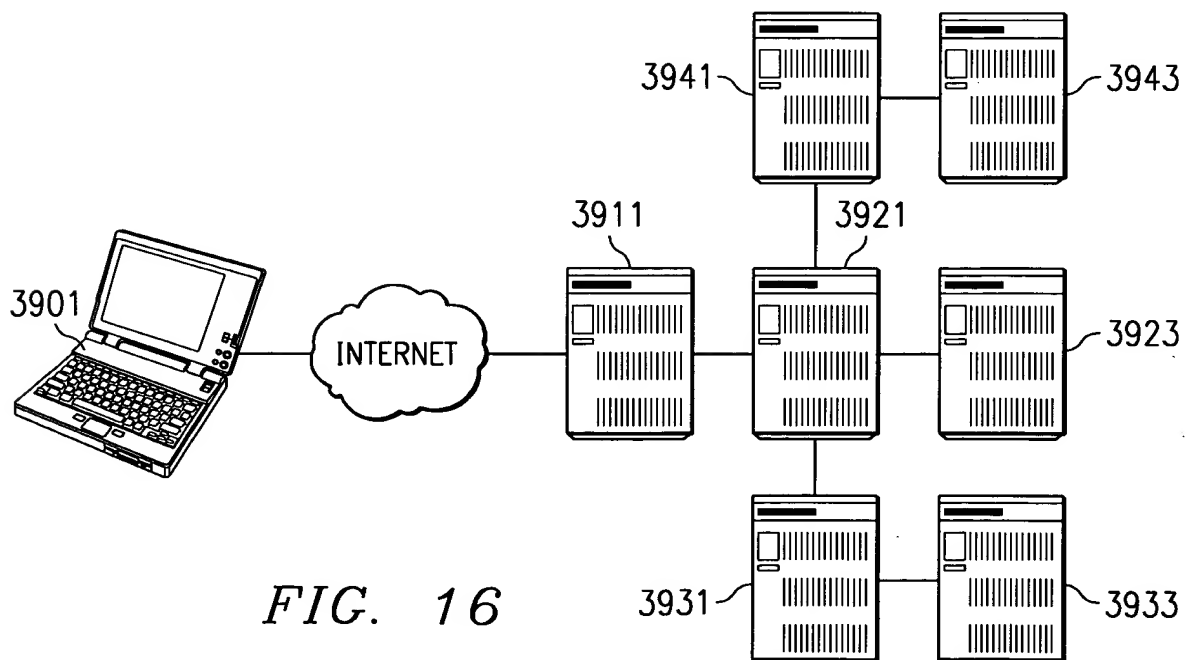


FIG. 16

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<table><tr><td><<enumeration>> signCoding</td></tr><tr><td><input checked="" type="checkbox"/> twosComplement</td></tr><tr><td><input checked="" type="checkbox"/> onesComplement</td></tr><tr><td><input checked="" type="checkbox"/> signMagnitude</td></tr><tr><td><input checked="" type="checkbox"/> zoneSigns</td></tr><tr><td><input checked="" type="checkbox"/> packedSigns</td></tr><tr><td><input checked="" type="checkbox"/> unsignedBinary</td></tr><tr><td><input checked="" type="checkbox"/> unsignedDecimal</td></tr></table>	<<enumeration>> signCoding	<input checked="" type="checkbox"/> twosComplement	<input checked="" type="checkbox"/> onesComplement	<input checked="" type="checkbox"/> signMagnitude	<input checked="" type="checkbox"/> zoneSigns	<input checked="" type="checkbox"/> packedSigns	<input checked="" type="checkbox"/> unsignedBinary	<input checked="" type="checkbox"/> unsignedDecimal	<table><tr><td><<enumeration>> lengthEncoding</td></tr><tr><td><input checked="" type="checkbox"/> fixedLength</td></tr><tr><td><input checked="" type="checkbox"/> lengthPrefixed</td></tr><tr><td><input checked="" type="checkbox"/> nullTerminated</td></tr></table>	<<enumeration>> lengthEncoding	<input checked="" type="checkbox"/> fixedLength	<input checked="" type="checkbox"/> lengthPrefixed	<input checked="" type="checkbox"/> nullTerminated	<table><tr><td><<enumeration>> floatType</td></tr><tr><td><input checked="" type="checkbox"/> Unspecified</td></tr><tr><td><input checked="" type="checkbox"/> IEEEExtendedIntel</td></tr><tr><td><input checked="" type="checkbox"/> IEEEExtendedAIX</td></tr><tr><td><input checked="" type="checkbox"/> IEEEExtendedOS390</td></tr><tr><td><input checked="" type="checkbox"/> IEEEExtendedAS400</td></tr><tr><td><input checked="" type="checkbox"/> IEEEnonextended</td></tr><tr><td><input checked="" type="checkbox"/> IBM390Hex</td></tr><tr><td><input checked="" type="checkbox"/> IBM400Hex</td></tr></table>	<<enumeration>> floatType	<input checked="" type="checkbox"/> Unspecified	<input checked="" type="checkbox"/> IEEEExtendedIntel	<input checked="" type="checkbox"/> IEEEExtendedAIX	<input checked="" type="checkbox"/> IEEEExtendedOS390	<input checked="" type="checkbox"/> IEEEExtendedAS400	<input checked="" type="checkbox"/> IEEEnonextended	<input checked="" type="checkbox"/> IBM390Hex	<input checked="" type="checkbox"/> IBM400Hex	<table><tr><td><<enumeration>> accessor</td></tr><tr><td><input checked="" type="checkbox"/> ReadOnly</td></tr><tr><td><input checked="" type="checkbox"/> WriteOnly</td></tr><tr><td><input checked="" type="checkbox"/> ReadWrite</td></tr><tr><td><input checked="" type="checkbox"/> NoAccess</td></tr></table>	<<enumeration>> accessor	<input checked="" type="checkbox"/> ReadOnly	<input checked="" type="checkbox"/> WriteOnly	<input checked="" type="checkbox"/> ReadWrite	<input checked="" type="checkbox"/> NoAccess	<table><tr><td><<enumeration>> packedDecimalSign</td></tr><tr><td><input checked="" type="checkbox"/> MVS</td></tr><tr><td><input checked="" type="checkbox"/> MVSCustom</td></tr><tr><td><input checked="" type="checkbox"/> NT_OS2_AIX</td></tr></table>	<<enumeration>> packedDecimalSign	<input checked="" type="checkbox"/> MVS	<input checked="" type="checkbox"/> MVSCustom	<input checked="" type="checkbox"/> NT_OS2_AIX	<table><tr><td><<enumeration>> bitModePad</td></tr><tr><td><input checked="" type="checkbox"/> mode16</td></tr><tr><td><input checked="" type="checkbox"/> mode24</td></tr><tr><td><input checked="" type="checkbox"/> mode31</td></tr><tr><td><input checked="" type="checkbox"/> mode32</td></tr><tr><td><input checked="" type="checkbox"/> mode64</td></tr><tr><td><input checked="" type="checkbox"/> mode128</td></tr></table>	<<enumeration>> bitModePad	<input checked="" type="checkbox"/> mode16	<input checked="" type="checkbox"/> mode24	<input checked="" type="checkbox"/> mode31	<input checked="" type="checkbox"/> mode32	<input checked="" type="checkbox"/> mode64	<input checked="" type="checkbox"/> mode128
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<input checked="" type="checkbox"/> signMagnitude																																										
<input checked="" type="checkbox"/> zoneSigns																																										
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<input checked="" type="checkbox"/> IEEEExtendedAIX																																										
<input checked="" type="checkbox"/> IEEEExtendedOS390																																										
<input checked="" type="checkbox"/> IEEEExtendedAS400																																										
<input checked="" type="checkbox"/> IEEEnonextended																																										
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<input checked="" type="checkbox"/> mode128																																										
<table><tr><td><<enumeration>> addrUnit</td></tr><tr><td><input checked="" type="checkbox"/> bit</td></tr><tr><td><input checked="" type="checkbox"/> byte</td></tr><tr><td><input checked="" type="checkbox"/> word</td></tr><tr><td><input checked="" type="checkbox"/> doubleWord</td></tr></table>	<<enumeration>> addrUnit	<input checked="" type="checkbox"/> bit	<input checked="" type="checkbox"/> byte	<input checked="" type="checkbox"/> word	<input checked="" type="checkbox"/> doubleWord																																					
<<enumeration>> addrUnit																																										
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<input checked="" type="checkbox"/> byte																																										
<input checked="" type="checkbox"/> word																																										
<input checked="" type="checkbox"/> doubleWord																																										

FIG. 12

FIG. 12

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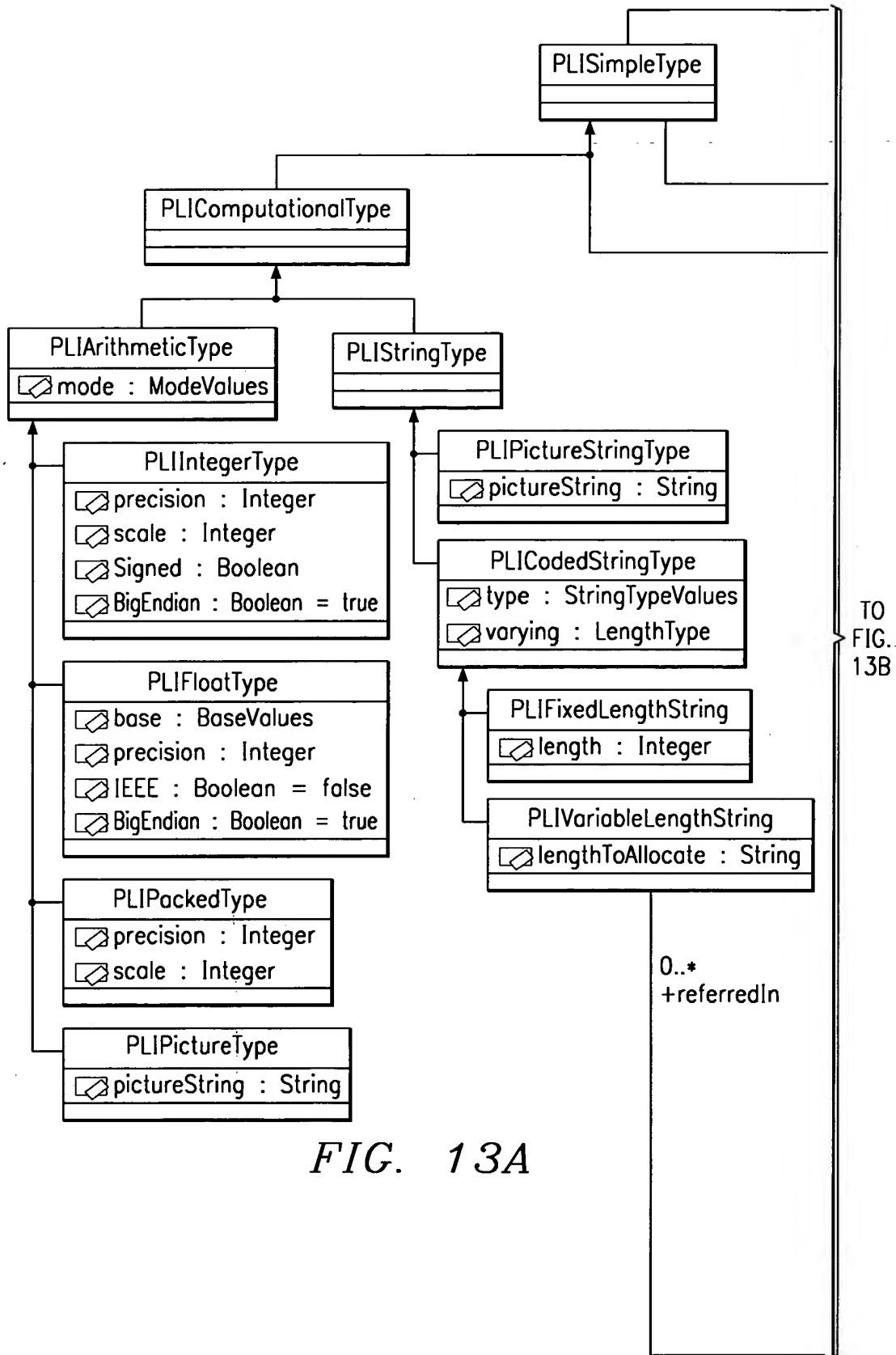
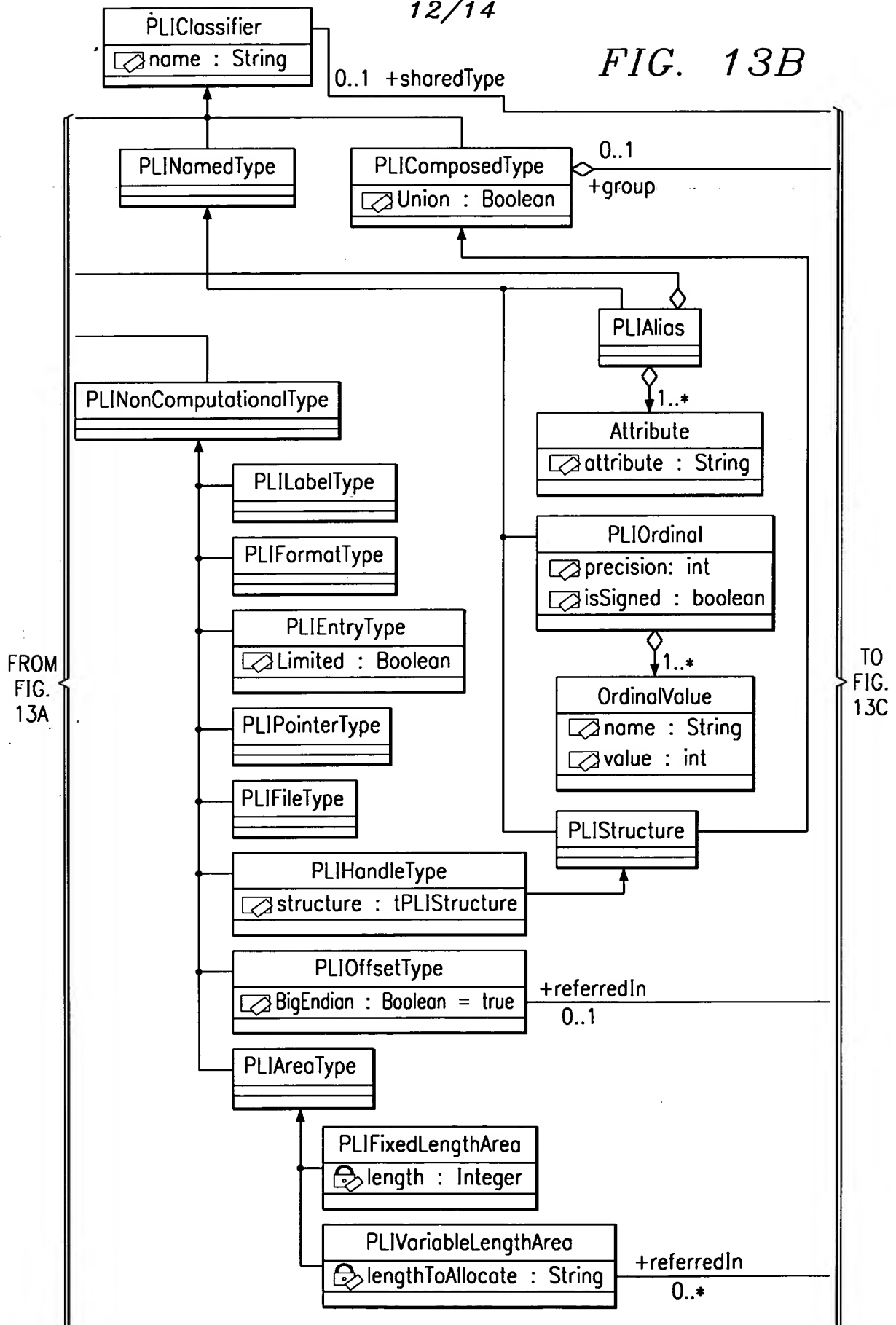


FIG. 13A

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FIG. 13B



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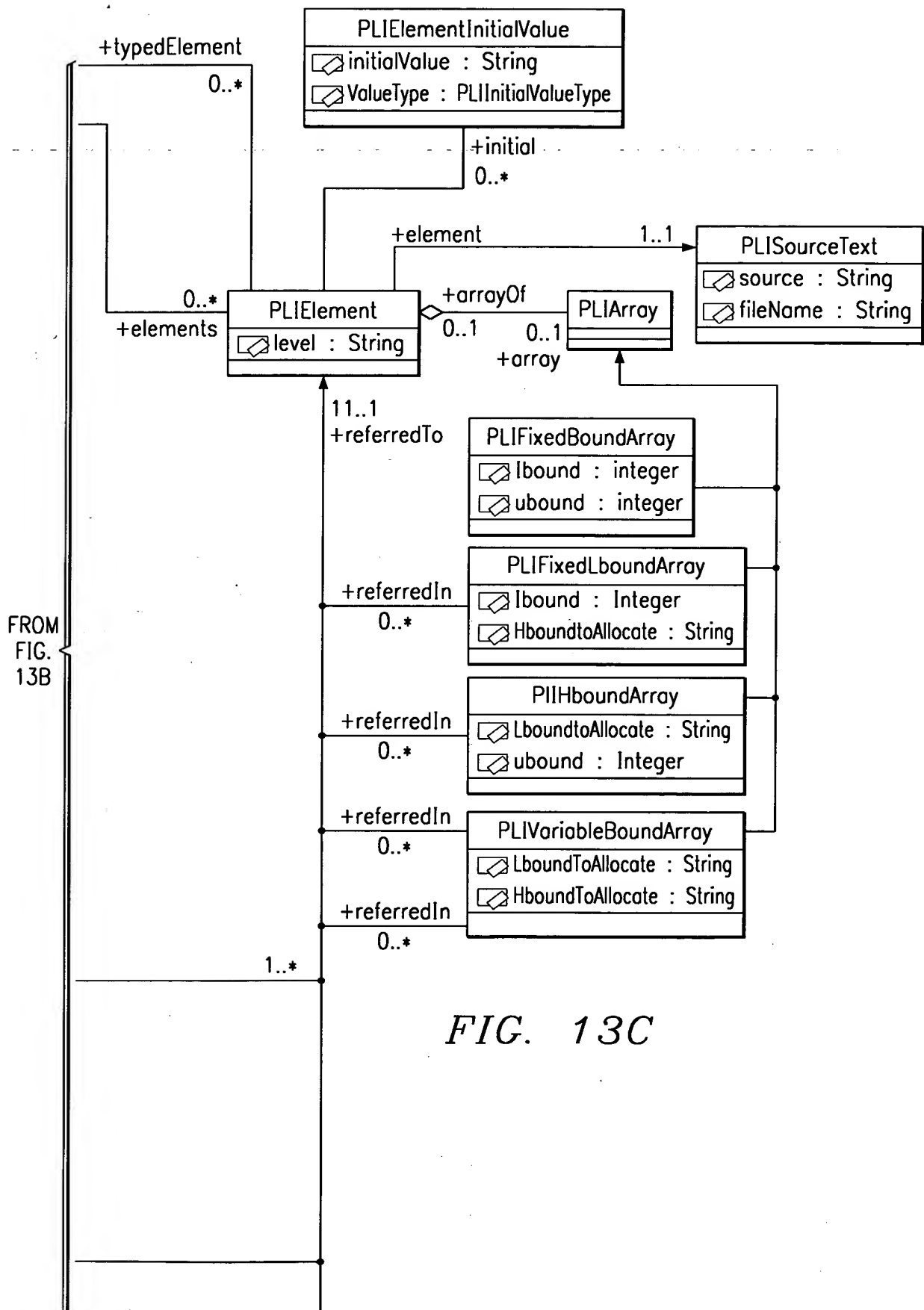


FIG. 13C

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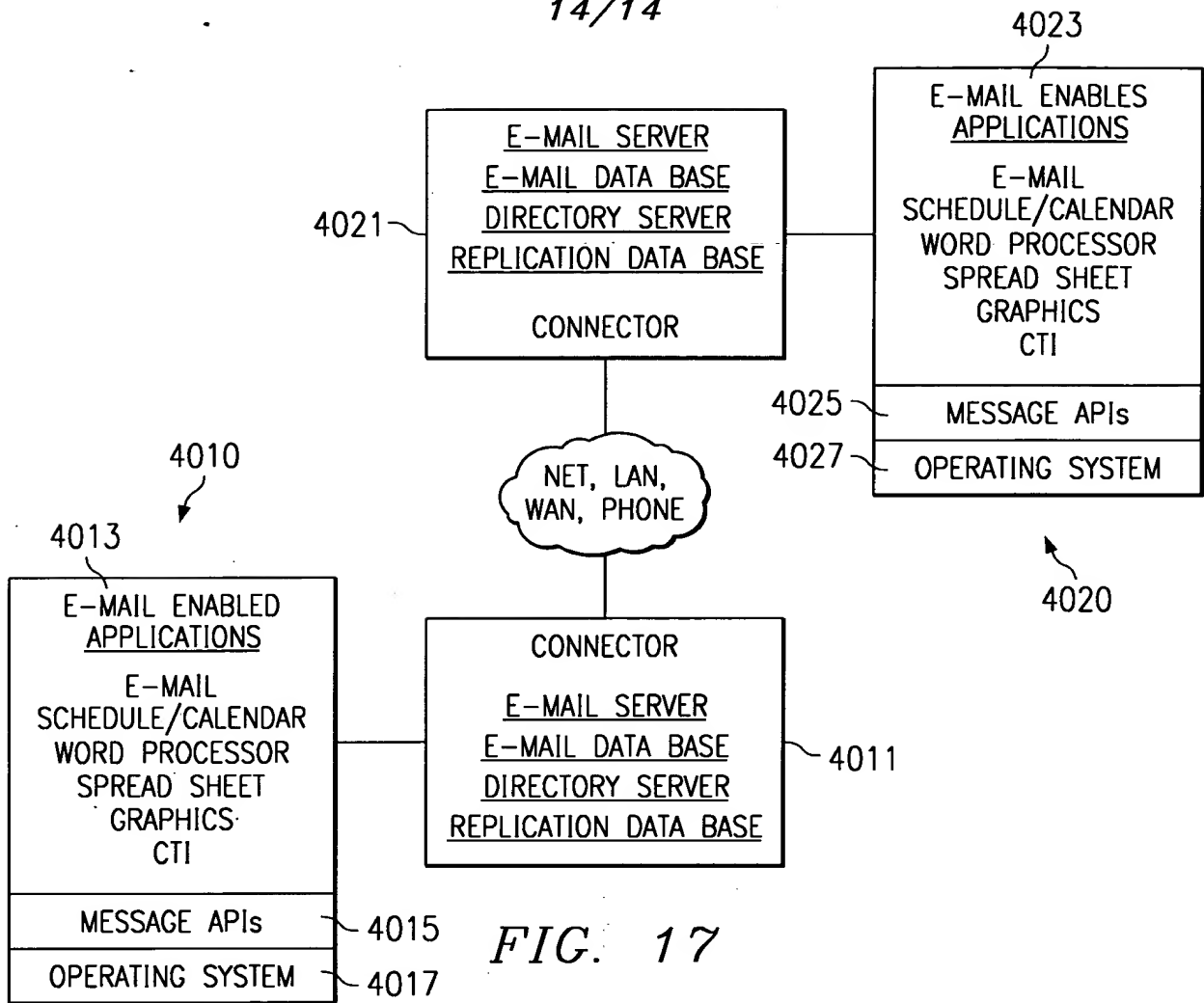


FIG. 17

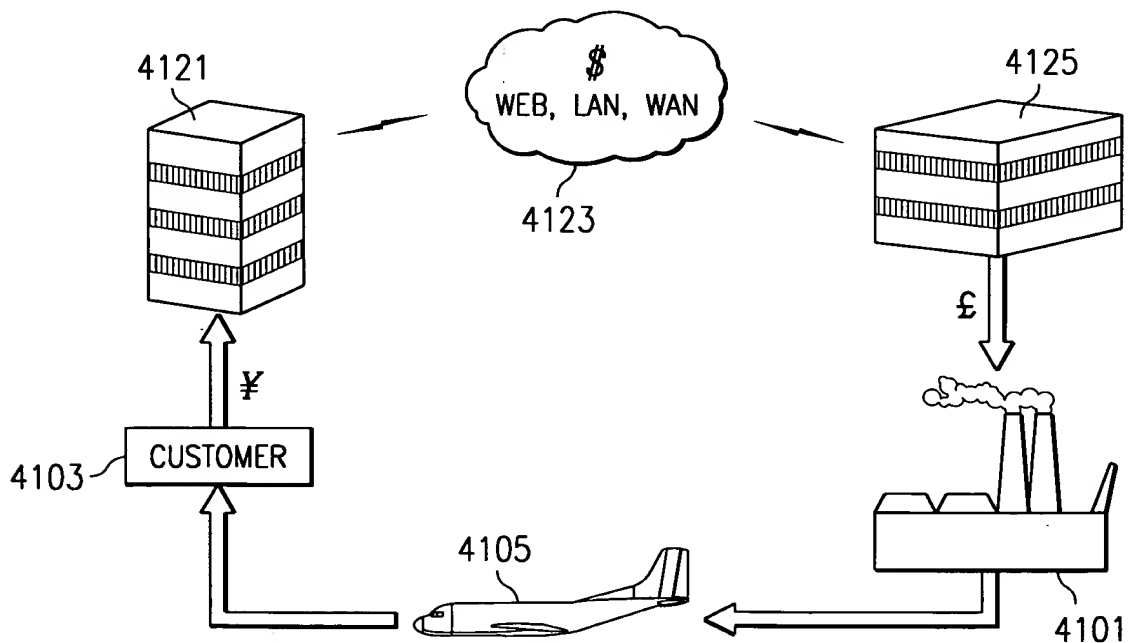


FIG. 18